Docket No. 242056US2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF: Masaru NUMANO

SERIAL NO: New Application GAU:

FILED: Herewith EXAMINER:

FOR: OPTICAL SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR 1.97

COMMISSIONER FOR PATENTS ALEXANDRIA, VIRGINIA 22313

SIR:

Applicant(s) wish to disclose the following information.

REFERENCES

- The applicant(s) wish to make of record the references listed on the attached form PTO-1449. Copies of the listed references are attached, where required, as are either statements of relevancy or any readily available English translations of pertinent portions of any non-English language references.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

RELATED CASES

- Attached is a list of applicant's pending application(s) or issued patent(s) which may be related to the present application. A copy of the patent(s), together with a copy of the claims and drawings of the pending application(s) is attached along with PTO 1449.
- ☐ A check is attached in the amount required under 37 CFR §1.17(p).

CERTIFICATION

- ☐ Each item of information contained in this information disclosure statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this statement.
- □ No item of information contained in this information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application or, to the knowledge of the undersigned, having made reasonable inquiry, was known to any individual designated in 37 CFR §1.56(c) more than three months prior to the filing of this statement.

DEPOSIT ACCOUNT

Please charge any additional fees for the papers being filed herewith and for which no check or credit card payment form is enclosed herewith, or credit any overpayment to deposit account number <u>15-0030</u>. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

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Tel. (703) 413-3000 Fax. (703) 413-2220 (OSMMN 05/03) DOCKET NO.: 242056US2

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STATEMENT OF RELEVANCY

Reference AA on Form PTO-1449:

A voltage clamp circuit for switched inductive loads is disclosed. In the box 70 of FIG. 1, the Zener diode 71 is shown to be connected in series with the diode 72 which connects the load side of the switch back to the switch control circuit.

Reference AO on Form PTO-1449:

A surge absorbing circuit including a Zener diode 3 and a diode 5 is disclosed. When a power MOSFET is switched off, the inductive load generate high voltage. The high voltage is clamped by the circuit. However, this surge absorbing circuit does not concern with an optical semiconductor device.

Reference AP on Form PTO-1449:

An electrostatic discharge protective device is disclosed. The base and the emitter of a parasitic PNP transistor that a Zener diode includes are biased reversely with a diode and a constant voltage. However the behavior of the parasitic PNP transistor does not concern with and irradiation of a light.

Form PTO 1449 U.S. DEPARTMENT OF COMMERCE				ATTY DOCKET NO.		SERIAL NO.	
(Modified) PATENT AND TRADEMARK OFFICE			242056US2		New Application		
				APPLICANT			
LIST OF	REFEF	RENCES CITED BY A	PPLICANT	Masaru NUMANO			
11.				FILING DATE Herewith		GROUP	
				U.S. PATENT DOCUMENTS			
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	ĀA	4,658,203	04/14/87	William P. FREYMUTH			
	AB						
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			FC	PREIGN PATENT DOCUMENTS			
		DOCUMENT NUMBER	DATE	COUNTRY		YE	TRANSLATION S NO
	AO	10-136564	05/22/98	Japan (with English Abstract)			X
	AP	6-216321	08/05/94	Japan (with English Abstract)			х
	AO						
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